HIGCE	Enrolment No. HASMUKH GOSWAMI COLLEGE OF ENGINEERING, VAHELAL MID SEMESTER EXAMINATION, SEPTEMBER-2016 Subject Code:2130902 Date:27/09/20 Subject Name: Analog Electronics Sem: 3 <sup>rd</sup> Time: 10:00 TO 10:50 Total Marks: 2	016 0
-	<ol> <li>Instructions:</li> <li>Attempt all questions.</li> <li>Make suitable assumptions wherever necessary.</li> <li>Figures to the right indicate full marks.</li> </ol>	
QUE.1	(A) MCQ 1) Efficiency of class B amplifier is a) 25 to 50% b) 50 to 78.5% c) 78.5% d) High 2) Voltage gain of ideal Inverting amplifier is (a) $1+R_f/R_1$ (b) $-R_f/R_1$ (c) $R_f/R_1$ (d) $1-R_f/R_1$ 3) Output impedance $R_0$ for Ideal Op-amp is (a) 0 (b) infinite (c) $1 \Omega$ (d) $100\Omega$	3
	(B) Write short note on : Cross – over distortion and describe a method to Minimize it.	3
QUE.2	<ul> <li>(A) Write short note on : current series/shunt feedback.</li> <li>(B) Explain the h-parameter model of CE amplifier and state the typical values of h-Parameter for the same and state merit and demerits of it.</li> </ul>	3 4
	(B) Discuss the frequency response characteristics Of RC couple amplifier and derive Expression.	4
QUE.3	(A) Derive equation of voltage gain, input resistance for closed loop inverting amplifier.	3
	(B) Define any seven OP-AMP parameters. OR	4
	<ul><li>(A) Explain ideal voltage transfer curve of an OP-AMP.</li><li>(B) Draw the circuit diagram of class B push pull and complementary symmetry power amplifier and explain its operation.</li></ul>	3 4